

IN THE U.S. PATENT AND TRADEMARK OFFICE  
I N F O R M A T I O N   S H E E T

1c978 U.S. PRO  
10/091530  
03/07/02

Applicant: GEMELLI, Riccardo  
PAVESI, Marco  
DE BLASIO, Giuseppe

Application No.:

Filed: March 7, 2002

For: A SYSTEM OF DISTRIBUTED MICROPROCESSOR INTERFACES TOWARD  
MACRO-CELL BASED DESIGNS IMPLEMENTED AS ASIC OR FPGA BREAD  
BOARDING AND RELATIVE COMMON BUS PROTOCOL

Priority Claimed Under 35 U.S.C. 119 and/or 120:

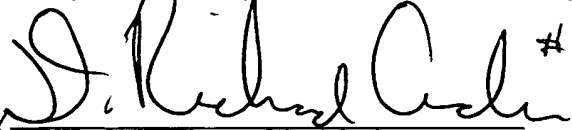
COUNTRY	DATE	NUMBER
UNITED KINGDOM	03/15/01	0106407.0

Send Correspondence to: BIRCH, STEWART, KOLASCH & BIRCH, LLP  
P. O. Box 747  
Falls Church, Virginia 22040-0747  
(703) 205-8000

The above information is submitted to advise the USPTO of all  
relevant facts in connection with the present application. A timely  
executed Declaration in accordance with 37 CFR 1.64 will follow.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By  #40,439

for MICHAEL K. MUTTER

Reg. No. 29,680

P. O. Box 747

Falls Church, VA 22040-0747

/sll

(703) 205-8000